METHOD AND SYSTEM FOR PERSONALIZED I/O DEVICE INITIALIZATION

ABSTRACT OF THE DISCLOSURE:

A method for controlling a plurality of I/O devices being
attached to a microprocessor by a special number and type of
5   interfaces is provided. A generic configurable chip is connected
to the I/O space of said microprocessor, said generic chip
comprising a switch matrix being adapted to assign said special
number and type of interfaces to each specific device when
initializing said device.